



250 NORTH WOLFE ROAD, SUNNYVALE CA 94085  
 TEL: 408.737.7200 FAX 408-737-8067

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NOTES/COMMENTS:

Docket #	P030.04.CIP.24+	By:	THD3	Faxing Date	August , 09, 2005
Ser. No:	10/765,406	Filing Date:	01/26/2004	Inv(s)	Darrell Rinerson et al.
Title:	Memory Array with High Temperature Wiring				

The following is being transmitted to the U.S. Patent Office:

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2.	Request for Continued Examination (RCE) Transmittal	1
3.	Supplement Application Data Sheet	5
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